

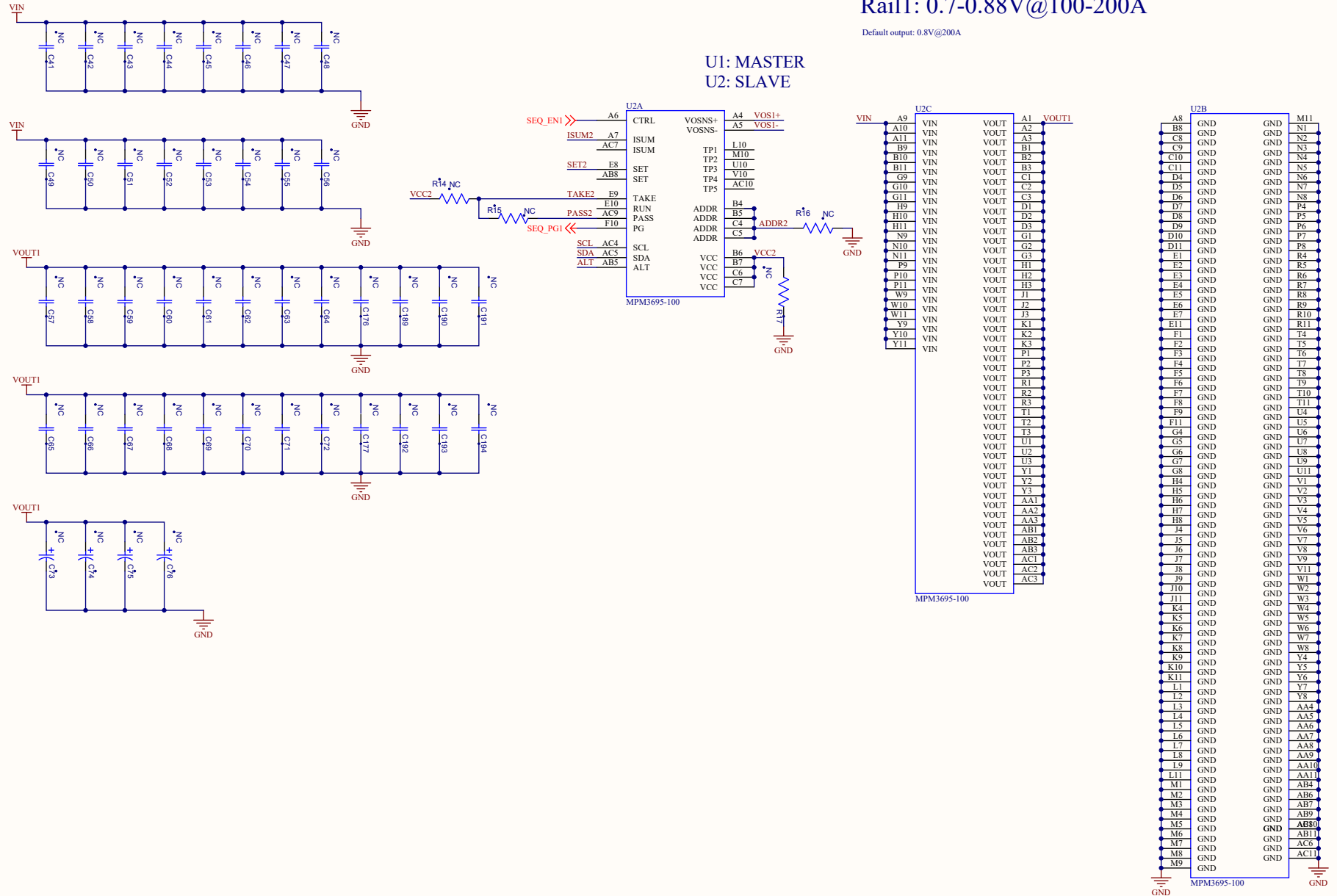
Title: <b>Versal_Reference_Design_High_Current</b>			
Size: <b>A3</b>	Engineer: <b>Ryan Zhang</b>		Revision: <b>R1.0</b>
Date: <b>9/1/2020</b>	Drawer: <b>Ryan Zhang</b>		Sheet: <b>2 of 7</b>
File: <b>\\cd-rw-home\Groups\AEP\Product\EVXLVA_02\02_Rail\1.SchDoc</b>			



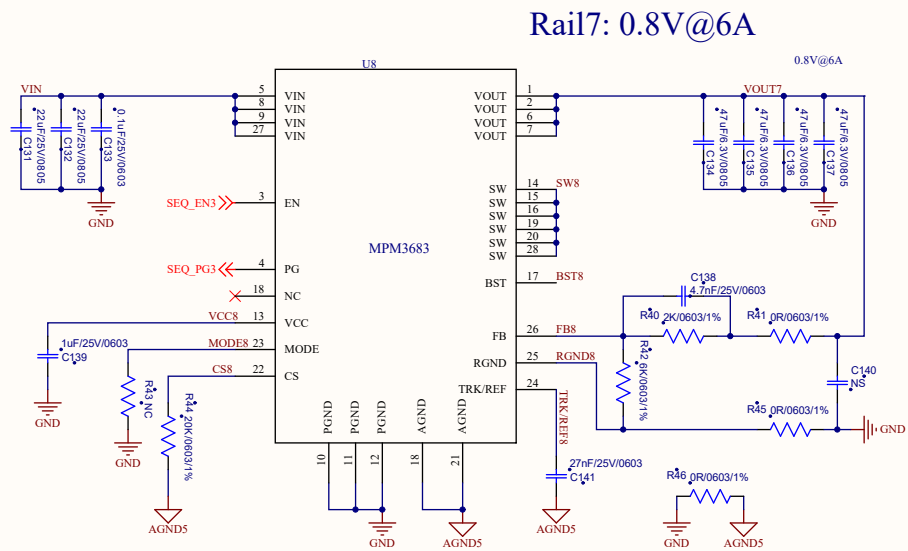
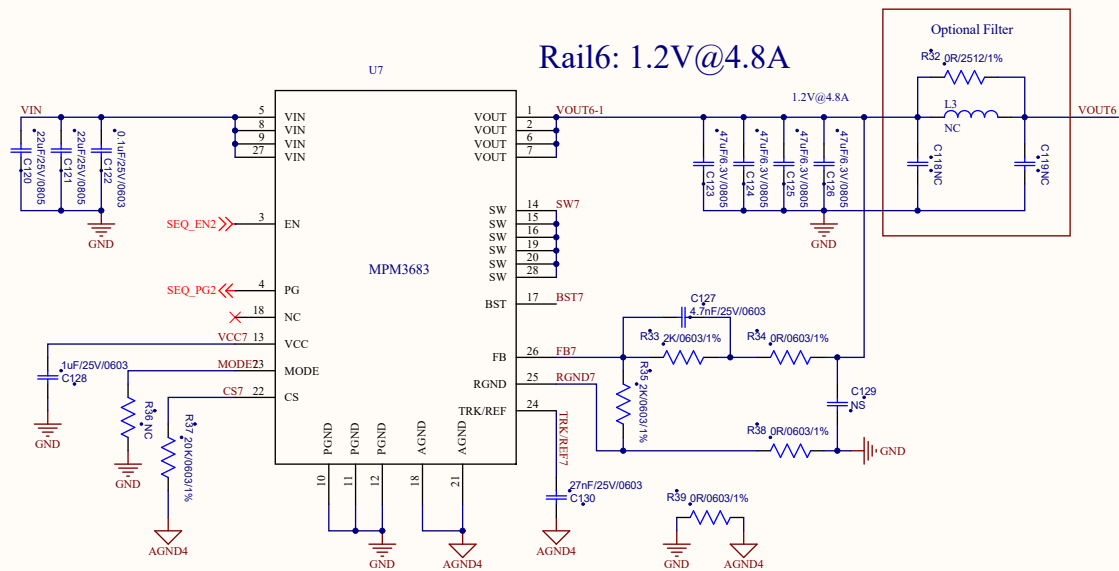
Rail1: 0.7-0.88V@100-200A

Default output: 0.8V@200A

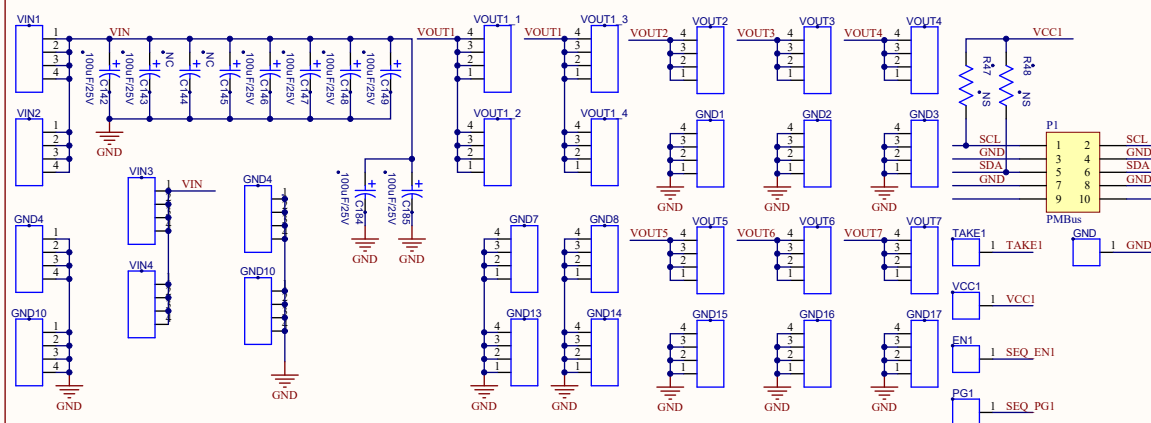
U1: MASTER  
U2: SLAVE



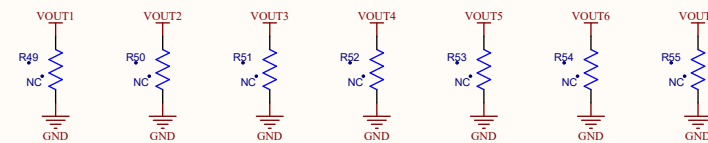




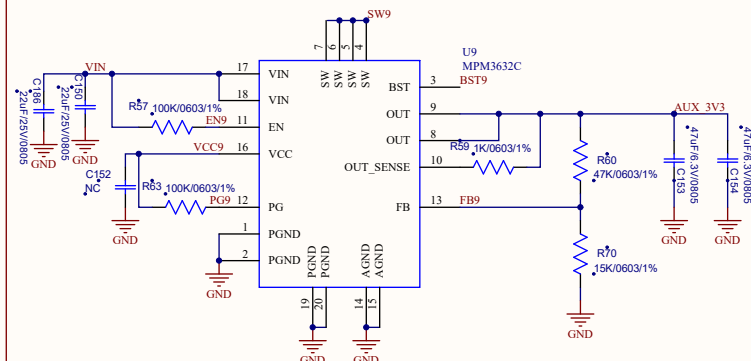
## Connector



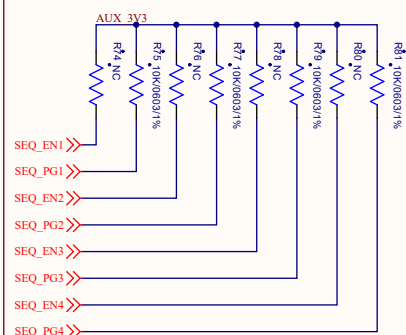
## Optional Dummy Load



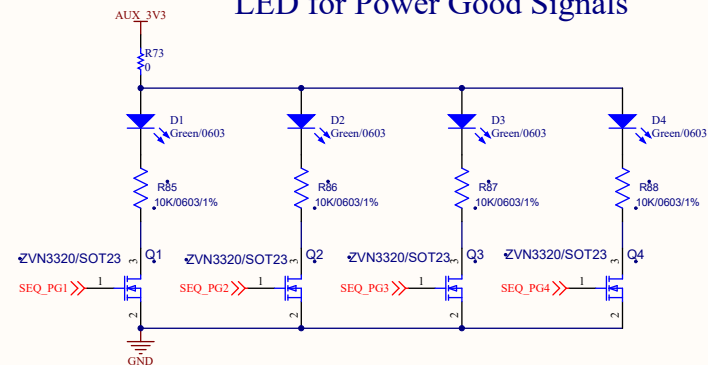
## Auxiliary Power Supply



## Pull up



## LED for Power Good Signals



## For test only

